



In re PATENT APPLICATION of

Applicant : Keiichi DEN

Appl. No. : 09/702,870

Filed : November 1, 2000

For : SEMICONDUCTOR DEVICE
CHIP-ON-CHIP STRUCTURE WITH
RADIATION NOISE SHIELD

Allowed : July 8, 2003

Atty. Dkt. : AI 261


Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability dated July 8, 2003, applicant hereby submits three sheets of corrected formal drawings, FIGS. 3, 4, and 5. The formal drawings incorporate the Examiner-approved proposed drawing changes submitted March 22, 2002. 2001.

Respectfully submitted,

October 8, 2003
Date


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SMR:pjl

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